|   | L# | Hit  | earch Text  | DBs                            |
|---|----|------|---|--------------------------------|
| 1 | L1 | 3320 | (257/691,723,724,777,9<br>24).ccls.                                       | USP<br>AT;<br>US-P<br>GPU<br>B |
| 2 | L2 | 1990 | (361/760,763,782,783).<br>ccls.   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 3 | L3 | 808  | 327/536.ccls.   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 4 | L4 | 771  | (363/59,60).ccls.   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 5 | L5 | 6531 | 1234  | USP<br>AT;<br>US-P<br>GPU<br>B |
| 6 | L6 | 266  | 5 and @pd>=20031003   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 7 | L7 | 34   | pump\$1) (voltage adj<br>(booster\$1 multipli\$6<br>doubl\$3)) (pump with | USP<br>AT;<br>US-P<br>GPU<br>B |

|    | L#  | Hit | Search Text   | DBs                            |
|----|-----|-----|---|--------------------------------|
| 8  | L8  | 232 | 6 not 7   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 9  | L9  | 91  | 8 and (passive inductor\$1 capacitor\$1 condenser\$1)         | USP<br>AT;<br>US-P<br>GPU<br>B |
| 10 | L10 | 141 | 8 not 9   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 11 | L11 | 2   | 10 and ((inductive capacitive) adj (element\$1 component\$1)) | USP<br>AT;<br>US-P<br>GPU<br>B |
| 12 | L12 | 139 | 10 not 11   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 13 | L13 | 7   | 12 and (inductive\$2<br>capacitive\$2)                        | USP<br>AT;<br>US-P<br>GPU<br>B |
| 14 | L14 | 132 | 12 not 13   | USP<br>AT;<br>US-P<br>GPU<br>B |

|   | L# | Hit  | S arch Text   | DB                             |
|---|----|------|---|--------------------------------|
| 1 | L1 | 1826 | (438/106,109).ccls.   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 2 | L4 | 5    | 1 and ((charge adj<br>pump\$1) (voltage adj<br>(booster\$1 multipli\$6<br>doubl\$3)) (pump with<br>circuit\$1)) | USP<br>AT;<br>US-P<br>GPU<br>B |
| 3 | L5 | 1821 | 1 not 4   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 4 | L6 | 343  | 5 and (passive inductor\$1 capacitor\$1 condenser\$1)   | USP<br>AT;<br>US-P<br>GPU<br>B |